

FIG.9A 60 Source Driving Circuit (Source Driver) 60a Shift Register 60ь Buffer 60c Sampling Circuit 62a 62b 62c Gate Driving Circuit (Gate Driver) Shift Register Level Shifter Buffer 61 Pixel Section

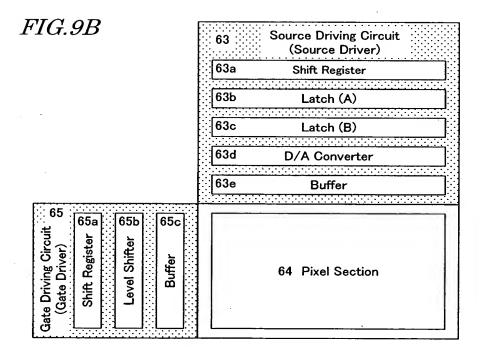
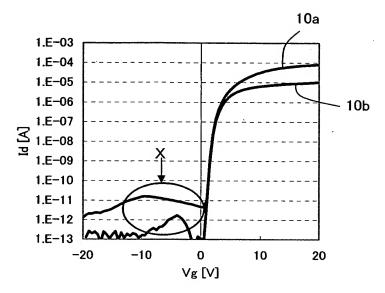
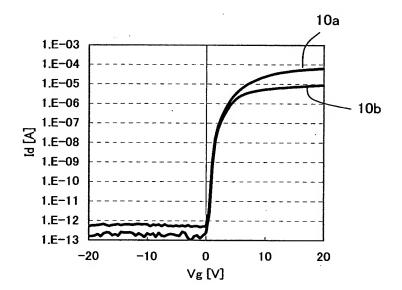
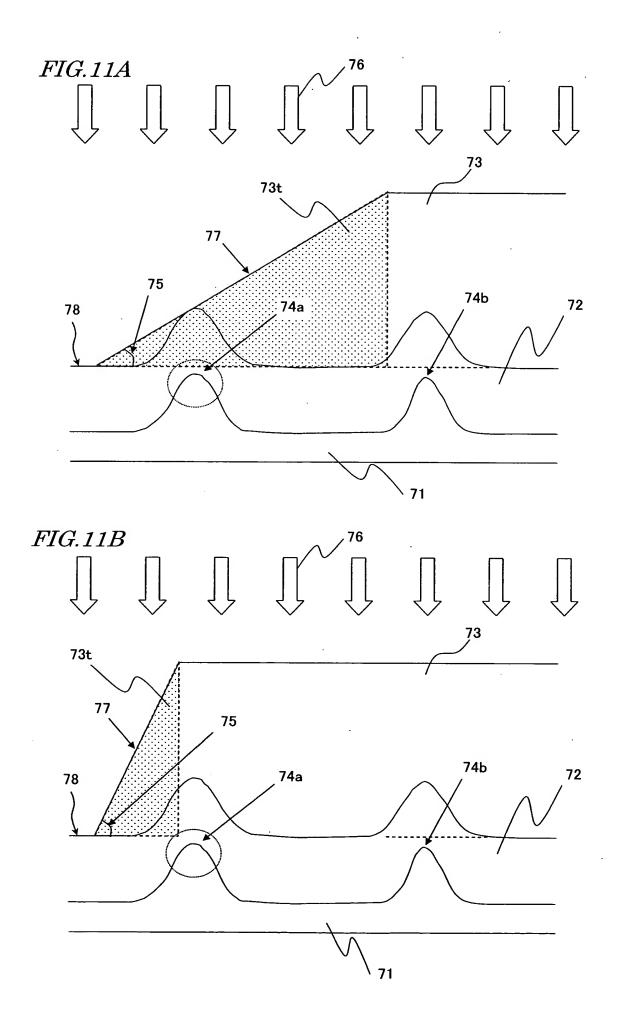


FIG.10A

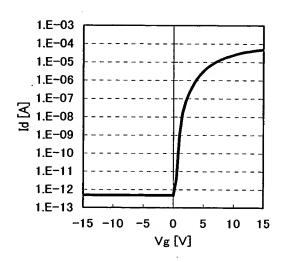


## *FIG.10B*

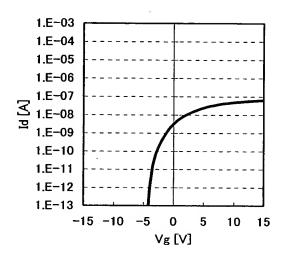




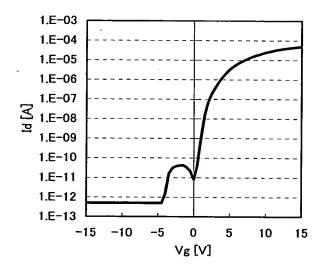
## FIG. 12A

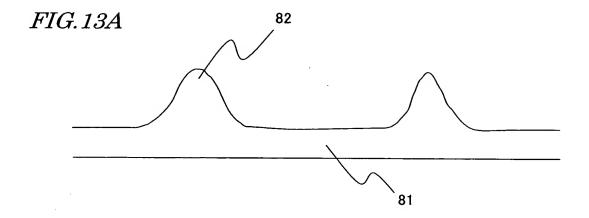


## FIG.12B

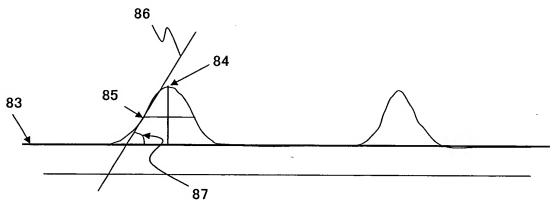


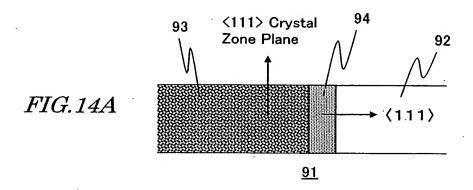
## *FIG.12C*

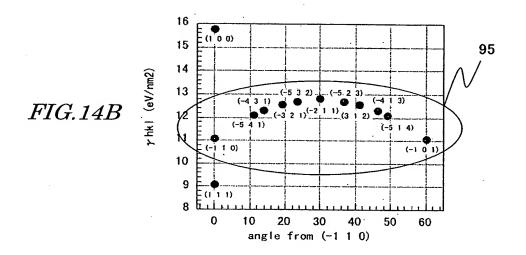


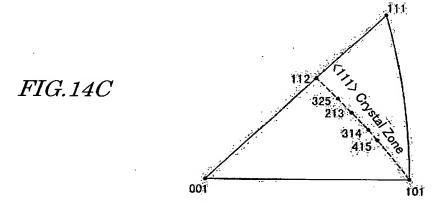


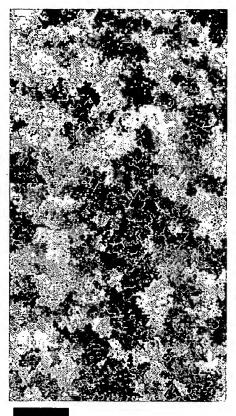






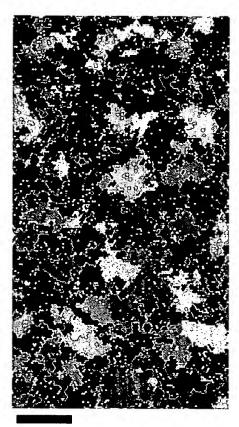






12.50 µm = 25 steps

*FIG.15A* 



12.50 μm = 25 steps

*FIG.15B* 

